

**REMARKS**

On July 5, 2006, the PTO issued a Notice of Drawing Inconsistency with Specification, stating that FIG. 1E is contained in the drawings but not listed in the Brief Description of the Drawings in the Specification.

As indicated above, Applicant specification has been amended to list Fig. 1E in the Brief Description of the Drawings.

Enclosed herewith, Applicant provides a copy of a Supplemental Notice of Allowability mailed by the PTO on June 20, 2006. An Examiner's Amendment appears on page 2 of the Supplemental Notice of Allowability. The Examiner's amendment reads as follows:

"The application has been amended as follows:

In specification:

On page 4, after the last line, insert -Figure 1E shows an individual chip that has been sawn or cut from the glass sheet semiconductor structure of Figure 1D--."

Thus, it appears that both the Examiner and Applicant have addressed the drawing inconsistency mention in the July 5, 2006, Notice of Drawing Inconsistency with Specification.

The Issue Fee in this application having been previously paid on September 20, 2005, it is respectfully requested that this application be expeditiously passed along to Issuance.

Should there be any questions or additional issues that need to be addressed in the application, it is requested that the undersigned be contacted immediately at (415) 772-4902, or [mpollock@stallmanpollock.com](mailto:mpollock@stallmanpollock.com).

Respectfully submitted,

STALLMAN & POLLOCK LLP

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By: Michael J. Pollock  
Michael J. Pollock  
Reg. No. 29,098

Attorneys for Applicant(s)